Amendments to the Claims:

Re-write the claims as set forth below. This listing of claims will replace all prior versions and listings, of claims in the application:

Listing of Claims:

1. (Canceled)

2. (Previously presented) The system of Claim 22 further comprising:

a memory having a first port coupled to the output video data port of the data storage controller and a second port coupled to the address port of the data storage controller.

3. (Original) The system of claim 2, wherein the memory is a frame buffer memory.

4. (Previously presented) The system of Claim 2 further comprising:

a system bus interface having a first port coupled to a third port of the memory, and a second port coupled to a fourth port of the memory.

5. (Previously presented) The system of Claim 4, wherein the third port of the memory is substantially the same as the second port of the memory.

6. (Previously presented) The system of Claim 22, wherein the digital video transport stream is a digital video broadcast transport stream.

7. (Previously presented) The system of Claim 6, wherein the control signals of the digital video transport stream include a clock signal, a synchronization signal, and a data valid signal.

8. (Original) The system of Claim 7, wherein the set of control signals of the transport stream interface control includes a start of field signal to indicate the start of a frame of video.

9. (Original) The system of Claim 8, wherein the set of control signals of the transport stream interface control signal includes a valid data output to indicate when data on the second output port of the transport stream interface control is active video data.

10. (Previously presented) The system of Claim 9, wherein the set of control signals of the data storage controller includes a valid vertical blanking interval signal to indicate when data on the output video data port of the data storage controller is present during a vertical blanking interval.

11. (Original) The system of Claim 6, further comprising:

a first video port to receive digital video of a first type, wherein the first type is not digital video broadcast stream video;

a first video interface control having an input coupled to the first video port, and having a first output port to provide the set of control signals, and a second output port to provide video graphics data; and

a select node coupled to the transport stream interface control and to the first video interface control.

12. (Original) The system of Claim 11, wherein the first video port is a zoom video port.

13. (Original) The system of Claim 11, wherein the first video port is a digital video stream port.

14. (Currently amended) A method of receiving video graphics data, the method comprising:

receiving a compressed transport stream associated with a digital video broadcast signal, the compressed transport stream having data signals and control signals;

generating a secondary set of memory control signals from the compressed transport stream's control signals;

storing at least a portion of the compressed transport stream data signals via a first bus in a memory buffer controlled by the secondary set of memory control signals wherein the memory buffer comprises a frame buffer that stores uncompressed data in a different mode of operation; and

sending at least the portion of the compressed transport stream data stored in the memory buffer via the first bus to a system bus.

15. (Previously presented) The method of Claim 14, further comprising:

wherein the receiving, generating and storing occur when in a first mode of operation;

during a second mode of operation, performing:

receiving a digital video signal having data signals and control signals, wherein the digital video signal is of a different type than the compressed transport stream;

generating the secondary set of control signals from the digital video signal's control signals; and

storing at least a portion of the digital video signal in the memory buffer based on the secondary set of control signals.

16. (Previously presented) The method of Claim 15, wherein the video signal is a zoom video signal.

17. (Canceled)

18. (Currently amended) A system for receiving a digital video broadcast signal, the system comprising:

a tuner to receive a digital video broadcast signal and to provide an analog output signal;

a demodulator coupled to receive the analog output signal from the tuner, and to provide a compressed transport stream;

a video graphics adapter having a transport stream port to receive the compressed transport stream and another transport stream, the video graphics adapter further includes a bus interface port coupleable to a central processing unit, and further includes a graphics engine and a video output port;

wherein the video graphics adapter is operative to store at least a portion of compressed transport stream data signals to be at first in frame buffer memory controlled by a secondary set of memory control signals derived from the compressed transport streams control signals, and storing uncompressed data in the frame buffer memory in a different mode of operation.

19. (Previously presented) The system of Claim 18, wherein the video graphics adapter includes:

memory operably coupled to the graphics engine, the transport stream port and to the bus interface port to store at least a portion of the compressed transport stream and coupled to store data for the graphics engine wherein the memory comprises frame buffer memory.

20. (Previously presented) The system of Claim 18, further comprising:

a central processor unit coupled to the bus interface port of the video graphics adapter; and

a transport demultiplexor coupled to a demodulator.

21. (Previously presented) A method of storing video data, the method comprising:

in a first mode of operation, storing pixel information in a frame buffer of a video graphics adapter, wherein one line of the frame buffer memory is representative of one line of a video image to be displayed; and

in a second mode of operation, storing compressed transport stream data in the frame buffer, wherein one line of the frame buffer memory is representative of one transport stream packet.

22. (Previously presented) A video graphics system comprising:

a transport stream port to receive a digital video transport stream, the digital video transport stream including a data stream and control signals;

a transport stream interface control having an input coupled to the transport stream port, and having a first output port to provide a set of control signals, and a second output port to provide video graphics data; and

a data storage controller having a first input port coupled to the first output port of the transport stream interface control to accept the set of control signals of the transport stream interface control, a second input port coupled to a second output port of a transport stream interface control to accept video graphics data, an output address port to provide an address value, an output video data port to provide video data, and at least one pair of a plurality of internal control ports to communicate control signals within the data storage controller.

23. (Previously presented) The system of Claim 7, wherein the control signals of the digital video transport stream further include an error signal, indicating there is an error in the transport stream.

24. (Previously presented) The method of Claim 14 wherein the secondary set of control signals from the compressed transport stream indicate at least: that a first byte of a transport stream packet is to be stored in the frame buffer, a first byte of the transport stream packet and a last byte of the transport stream packet.

25. (Previously presented) A method of receiving video graphics data, the method comprising:

receiving a compressed transport stream associated with a digital video broadcast signal, the compressed transport stream having data signals and control signals;

generating a secondary set of control signals from the compressed transport stream's control signals;

storing at least a portion of the compressed transport stream data signals via a first bus in a memory buffer controlled by the secondary set of control signals wherein the memory buffer comprises a frame buffer that stores uncompressed data in a different mode of operation;

sending the at least portion of the compressed transport stream data stored in the memory buffer via the first bus to a system bus;

wherein generating the secondary set of control signals comprises generating frame based control signals to store the compressed transport stream data in the frame buffer wherein transport stream packets containing compressed data are stored in the frame buffer memory based on display frame control signals; and

wherein the secondary control signals include a start of frame control signal indicating that a first byte of transport stream packet is to be stored in the frame buffer, a start of active frame control signal indicating the first byte of transport stream packet, and end of active frame control signal indicating the last byte of a transport stream packet and a deactive control signal that is asserted to indicate invalid bytes are present in the compressed transport stream.

26. (Previously presented) The video graphics system of Claim 22 comprising:

a packer;

a window control; and

an address generator, wherein the packer is operative to pack data into a format that is compatible with a frame buffer memory, the window controller is operative to control the amount of data written into the frame buffer memory, and the address generator is operative to generate proper frame buffer addresses for data from the transport stream, and wherein the frame buffer addresses comprise addresses corresponding to a predefined area of the frame buffer.